

FIG. 1

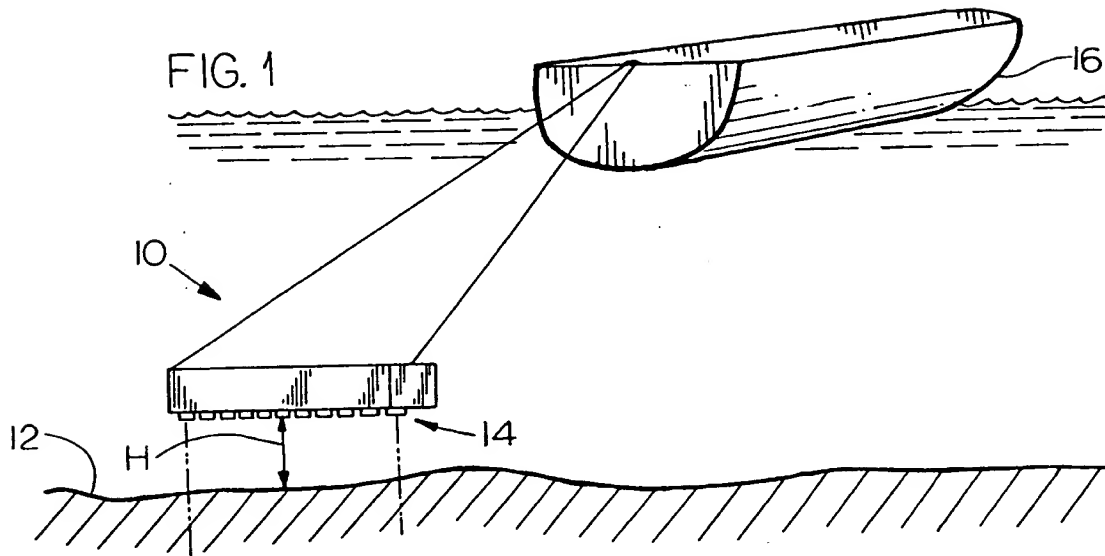


FIG. 2

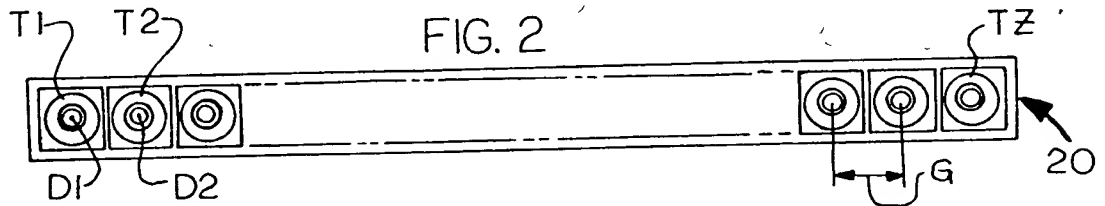
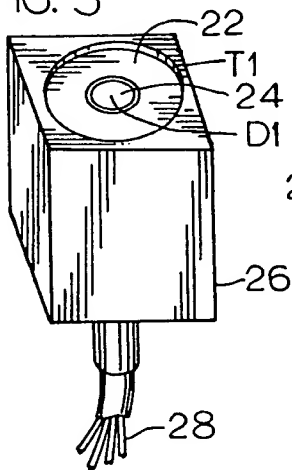
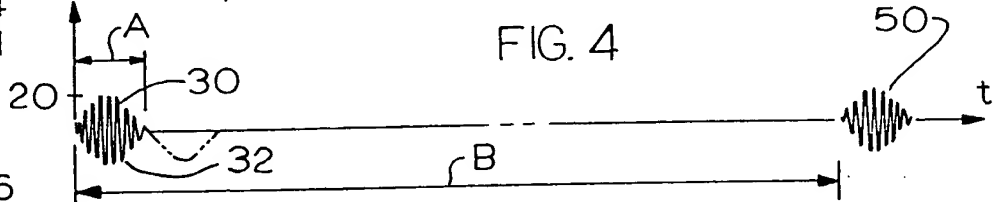


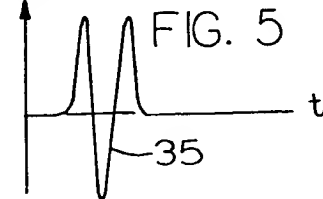
FIG. 3



AMPLITUDE
WATTS/CM.²



AMPL.



AMPL.

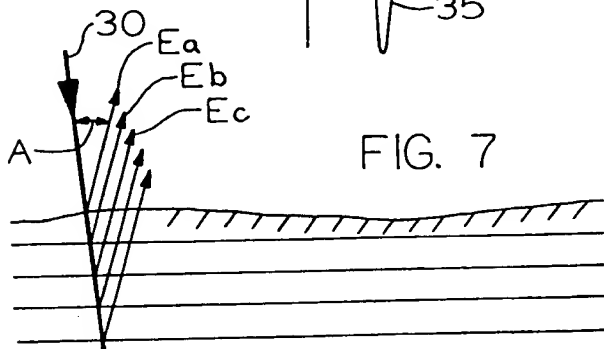
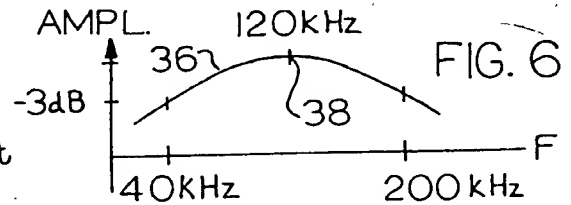


FIG. 7

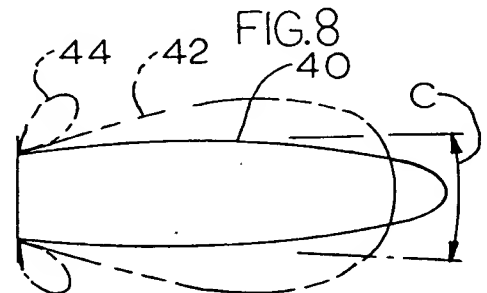


FIG. 8

FIG. 9

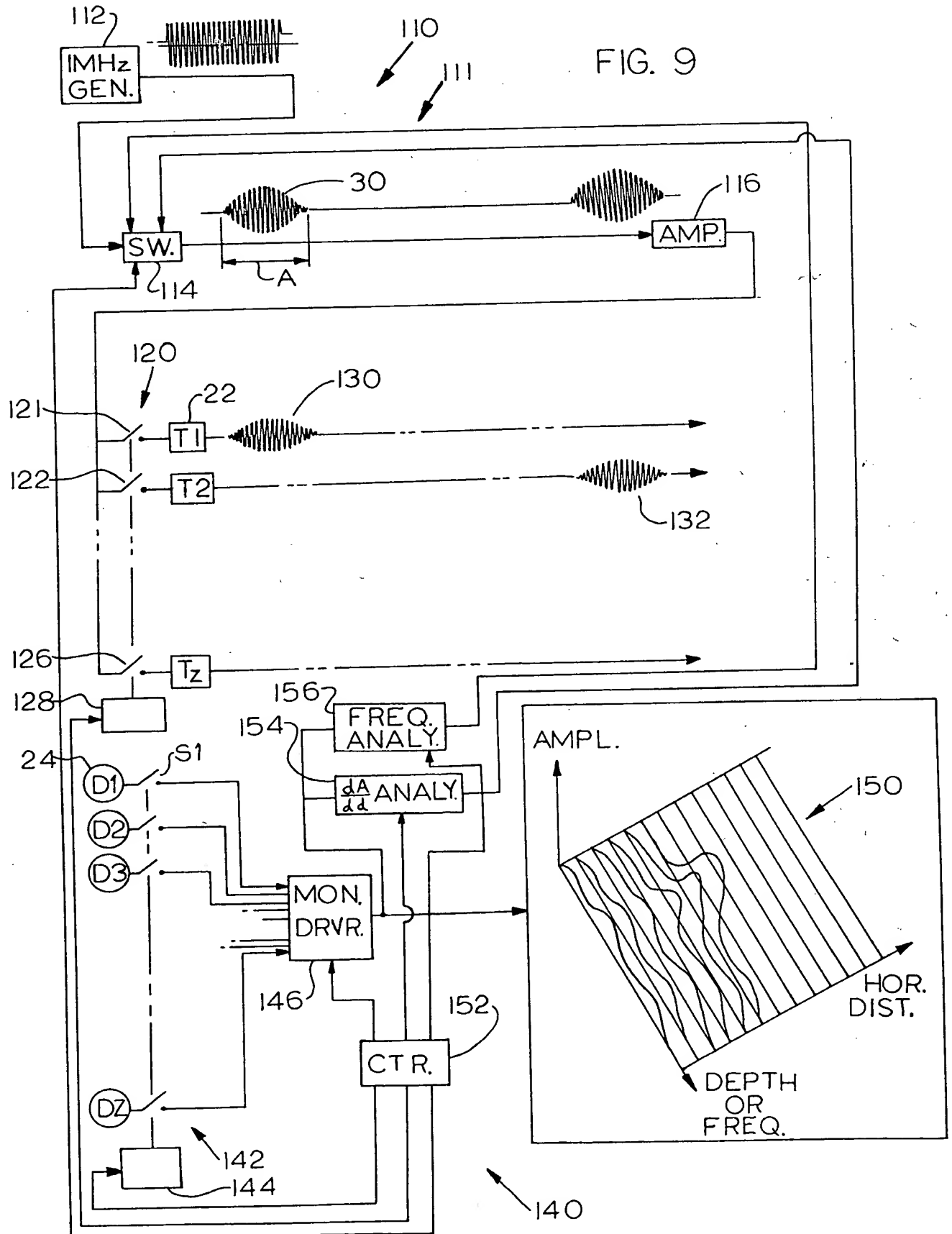


FIG. 10

176

183

CENTRAL COMPUTER

178

DISPLAY PROCESSING UNIT

160

FUZZY EXPERT SYSTEM

DATA PROCESS ALGORITHMS & FRACTAL PROCESSING

PARALLEL PROCESS UNIT

174

180

182

187

186

RS-485

162

RELAY

194

DC SUPPLY

195

BATTERY

188

PC CONT.

166

WAVE-FORM GEN.

164

DSP CHIPS

186

POWER AMP

172

184

DECODER

RELAYS

168

PRE-AMP

185

MUX

FIG. 11

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graph LR; MP[MICRO-PROCESSOR CONT.] --> ITH[INTERFACE TO HOST]; MP --> FDB[FIFO DATA BUFFER]; PCC[PROGRAMMABLE CLOCK CONTROLLER] --> FDB; TC[TRIGGER CIRCUIT] --> PCC; FDB --> HSDAC[HIGH SPEED D/A CONVERTER]; HSDAC --> M[MODULATOR 192]; SWG[SINE WAVE GENERATOR 190] --> M; M --> PA[PROGRAMMABLE ATTENUATOR]; PA --> PREA[PRE AMP]; PREA --> POWA[POWER AMP];
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FIG. 12

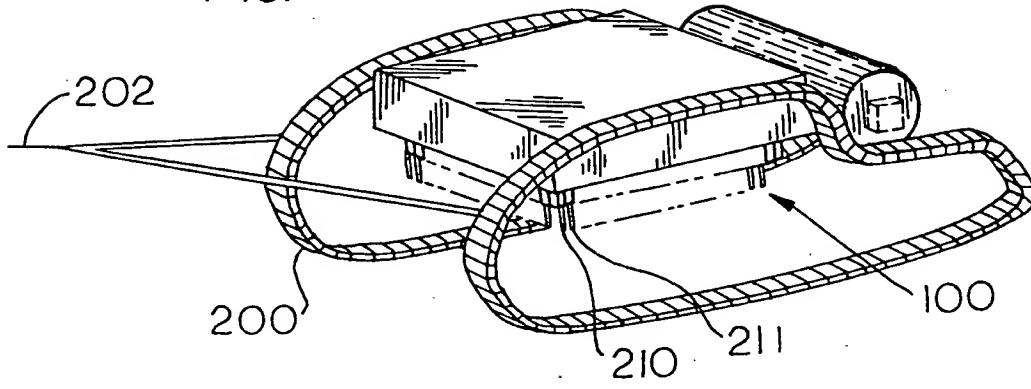
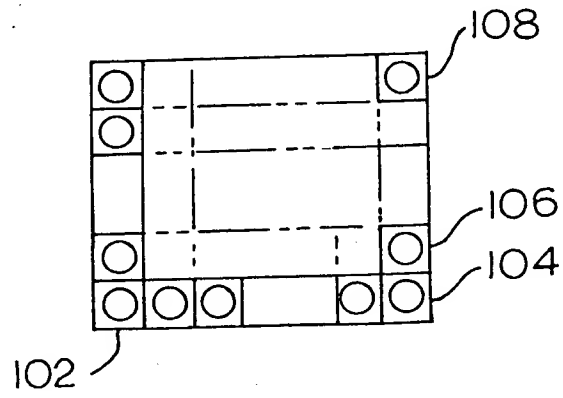


FIG. 12A



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